

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L10	1	crs with col with "same" with pin and (reduc\$3 less\$3 decreas\$3 lowe\$5) with pin with count	US-PGPUB; USPAT	OR	ON	2005/03/18 14:33
L11	1	crs with col with "same" with pin and (reduc\$3 less\$3 decreas\$3 lowe\$5) with pin	US-PGPUB; USPAT	OR	ON	2005/03/18 14:33
L12	1	crs with col with "same" and (reduc\$3 less\$3 decreas\$3 lowe\$5) with pin	US-PGPUB; USPAT	OR	ON	2005/03/18 14:33
L13	1	crs same col same "same" and (reduc\$3 less\$3 decreas\$3 lowe\$5) with pin	US-PGPUB; USPAT	OR	ON	2005/03/18 14:36
L14	55	crs same "same" and col and (reduc\$3 less\$3 decreas\$3 lowe\$5) with pin	US-PGPUB; USPAT	OR	ON	2005/03/18 14:36
L15	1	crs with "same" with col and (reduc\$3 less\$3 decreas\$3 lowe\$5) with pin	US-PGPUB; USPAT	OR	ON	2005/03/18 14:36
L16	27	crs with col and (reduc\$3 less\$3 decreas\$3 lowe\$5) with pin	US-PGPUB; USPAT	OR	ON	2005/03/18 14:37
L17	22	crs with col and (reduc\$3 less\$3 decreas\$3 lowe\$5) with pin	USPAT	OR	ON	2005/03/18 14:37
S5	19	(daniel with dove).in.	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2005/03/16 14:01
S8	38	(reduc\$3 decreas\$3) with pin and pin with count\$3 and mii	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2005/03/16 14:04
S9	35	(reduc\$3 decreas\$3) with pin and pin with count\$3 and mii and (switch\$3 port)	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2005/03/16 14:05
S10	25	(reduc\$3 decreas\$3) with pin and pin with count\$3 and mii and (switch\$3 port)	USPAT	OR	ON	2005/03/16 14:07
S11	4	(reduc\$3 decreas\$3) with pin and pin with count\$3 and mii and (switch\$3 port) and giga\$9	USPAT	OR	ON	2005/03/16 14:10
S12	6	(reduc\$3 decreas\$3) with pin and pin with count\$3 and mii and (switch\$3 port) and giga\$9	US-PGPUB	OR	ON	2005/03/16 14:14

S13	3	(reduc\$3 lowe\$9 decreas\$3) with pin and lowe\$9 with pin with count\$3 and mii and (switch\$3 port) and giga\$9	US-PGPUB	OR	ON	2005/03/16 14:15
S14	0	S13 not (S12 S11)	US-PGPUB	OR	ON	2005/03/16 14:15
S16	5	(reduc\$3 lowe\$9 decreas\$3) with pin and lowe\$9 with pin with count\$3 and mii and (switch\$3 port) and giga\$9	US-PGPUB; USPAT	OR	ON	2005/03/16 14:15
S17	0	S16 not (S12 S11)	US-PGPUB	OR	ON	2005/03/16 14:15
S18	8	(reduc\$3 lowe\$9 decreas\$3) with pin and lowe\$9 with pin with count\$3 and mii and (switch\$3 port)	US-PGPUB; USPAT	OR	ON	2005/03/16 14:46
S19	1	S18 not (S12 S11)	US-PGPUB	OR	ON	2005/03/16 14:16
S20	3	S18 not (S12 S11)	US-PGPUB; USPAT	OR	ON	2005/03/16 14:18
S27	27	((reduc\$3 lowe\$9 decreas\$3) with (pin with count pin\$3count)).ti.	US-PGPUB; USPAT	OR	ON	2005/03/18 09:10
S28	17	((reduc\$3 lowe\$9 decreas\$3) with (pin with count pin\$3count)).ti.	USPAT	OR	ON	2005/03/16 14:23
S29	10	((reduc\$3 lowe\$9 decreas\$3) with (pin with count pin\$3count)).ti.	US-PGPUB	OR	ON	2005/03/16 14:22
S30	6	(S28 S29) and ("370".clas. "709". clas.)	USPAT	OR	ON	2005/03/16 14:23
S31	8	pin with count\$3 and mii and "28" with "12"	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2005/03/16 14:32
S32	21	pin with count\$3 and \$3mii and "28" with "12"	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2005/03/16 14:31
S33	6	pin with count\$3 and mii and "28" with "12"	USPAT	OR	ON	2005/03/16 14:57
S36	3	pin with count\$3 and mii and "12" with pin	USPAT	OR	ON	2005/03/16 14:58
S37	1	pin with count\$3 and mii and "12" with pin	US-PGPUB	OR	ON	2005/03/16 15:08
S46	104	fall\$3 with ris\$3 with clock\$3 and (reduc\$3 subtract\$3 decreas\$3) with pin with count	US-PGPUB; USPAT	OR	ON	2005/03/16 15:06
S47	2	fall\$3 with ris\$3 with clock\$3 and (reduc\$3 subtract\$3 decreas\$3) with pin with count and "370".clas.	USPAT	OR	ON	2005/03/16 15:06

S48	1	pin with count\$3 and mii and "12" with pin and ris\$3 and fall\$3	US-PGPUB	OR	ON	2005/03/16 15:10
S49	164	"802.3z"	US-PGPUB	OR	ON	2005/03/16 15:11
S50	10	"802.3z" and pin with count	US-PGPUB	OR	ON	2005/03/16 15:11
S51	10	S50 not hewlett.as.	US-PGPUB	OR	ON	2005/03/16 15:11
S52	4	S51 and multiplex\$3	US-PGPUB	OR	ON	2005/03/16 15:12
S53	3	S52 and (mac or mii or \$3mii)	US-PGPUB	OR	ON	2005/03/16 15:20
S54	2	S52 and ("13" "12") adj5 pin	US-PGPUB	OR	ON	2005/03/16 16:04
S55	1	S52 and ("13" "12") adj5 pin and crs and col	US-PGPUB	OR	ON	2005/03/16 16:05
S56	1	S52 and ("13" "12") adj5 pin and crs and col and fall\$3 and ris\$3	US-PGPUB	OR	ON	2005/03/16 16:06
S57	1	S52 and ("13" "12") adj5 pin and crs and col and fall\$3 and ris\$3 and reduc\$3 with pin	US-PGPUB	OR	ON	2005/03/16 16:07
S58	2	pin near5 count\$3 and crs and col and fall\$3 and ris\$3 and (reduc\$3 lowe\$5) with pin	US-PGPUB	OR	ON	2005/03/16 16:08
S59	9	pin near5 count\$3 and crs and col and fall\$3 and ris\$3 and (reduc\$3 lowe\$5) with pin	US-PGPUB; USPAT	OR	ON	2005/03/16 16:08
S60	9	pin near5 count\$3 and crs and col and fall\$3 and ris\$3 and multiplex\$3 and (reduc\$3 lowe\$5) with pin	US-PGPUB; USPAT	OR	ON	2005/03/16 16:09
S61	8	S60 not dove.in.	US-PGPUB; USPAT	OR	ON	2005/03/16 16:09
S62	9	(less\$3 with (pin with count pin\$3count)) and "370".clas.	USPAT	OR	ON	2005/03/18 09:12
S63	17	((reduc\$3 lowe\$9 decreas\$3) with (pin with count pin\$3count)).ti.	USPAT	OR	ON	2005/03/18 09:11
S64	10	((reduc\$3 lowe\$9 decreas\$3) with (pin with count pin\$3count)).ti.	US-PGPUB	OR	ON	2005/03/18 09:11
S65	6	(S63:S64) and ("370".clas. "709".clas.)	USPAT	OR	ON	2005/03/18 09:11
S66	9	S62 not S65	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2005/03/18 09:30
S67	10	((reduc\$3 lowe\$9 decreas\$3) with (pin with count pin\$3count)).ti.	US-PGPUB	OR	ON	2005/03/18 09:11

S68	3325	(ten "10") with bit with (interface mii)	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2005/03/18 09:31
S69	2374	(ten "10") with bit with (interface mii)	USPAT	OR	ON	2005/03/18 09:32
S70	8	(less\$3 with (pin with count pin\$3count)) and "370".clas.	US-PGPUB	OR	ON	2005/03/18 09:31
S71	191	(ten "10") with bit with (interface mii) and giga\$7 and switch\$3	USPAT	OR	ON	2005/03/18 09:54
S72	13	(ten "10") with bit with (interface mii) and giga\$7 and switch\$3 and (reduc\$3 less\$3 decreas\$3 lowe\$7) with pin	USPAT	OR	ON	2005/03/18 09:55
S73	13	S72 and (sid\$3 edg\$3) with (clock\$3 tim\$3)	USPAT	OR	ON	2005/03/18 09:35
S74	1	S72 and (both double) with (sid\$3 edg\$3) with (clock\$3 tim\$3)	USPAT	OR	ON	2005/03/18 09:54
S75	1	S72 and (both double) with (sid\$3 edg\$3) with (clock\$3 tim\$3) and fall\$3 and ris\$3	USPAT	OR	ON	2005/03/18 09:52
S76	1	S75 and ("2.5" "25" "125") with mhz and "500" and (ns nanosecond nano?second)	USPAT	OR	ON	2005/03/18 10:14
S77	198	(ten "10") with bit with (interface mii) and giga\$7 and switch\$3	US-PGPUB	OR	ON	2005/03/18 09:54
S78	0	S77 and (both double) with (sid\$3 edg\$3) with (clock\$3 tim\$3)	USPAT	OR	ON	2005/03/18 09:54
S79	17	(ten "10") with bit with (interface mii) and giga\$7 and switch\$3 and (reduc\$3 less\$3 decreas\$3 lowe\$7) with pin	US-PGPUB	OR	ON	2005/03/18 10:02
S80	0	S79 and ("2.5" "25" "125") with mhz and "500" and (ns nanosecond nano?second)	USPAT	OR	ON	2005/03/18 10:20
S81	0	S79 and ("2.5" "25" "125") with mhz and (ns nanosecond nano?second)	USPAT	OR	ON	2005/03/18 09:56
S82	0	S79 and ("2.5" "25" "125") with mhz	USPAT	OR	ON	2005/03/18 09:56
S83	0	S79 and (ns nanosecond nano?second)	USPAT	OR	ON	2005/03/18 09:57
S84	2	?gmii and ?tbi and reduc\$3 with pin and clock and "12" with (pin termina\$4)	US-PGPUB	OR	ON	2005/03/18 10:04

S85	1	?gmii and ?tbi and reduc\$3 with pin and clock and "12" with (pin termina\$4)	USPAT	OR	ON	2005/03/18 10:04
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